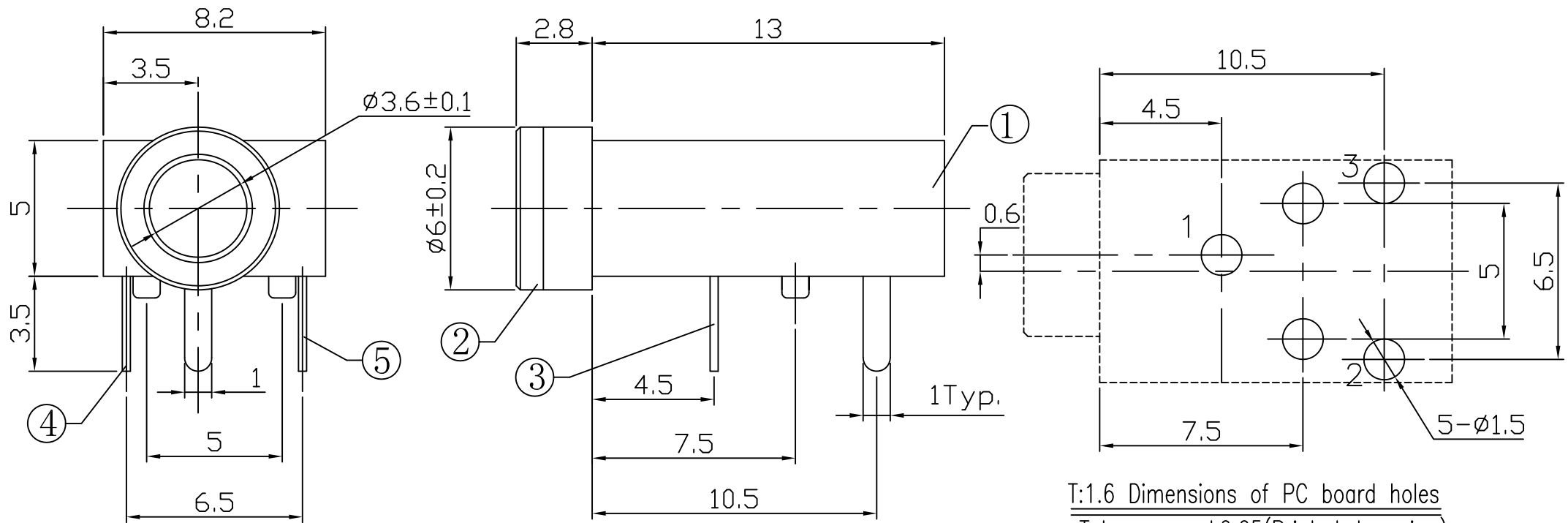


* REVISIONS				
LTR	DESCRIPTION	DATE	REVISED	APPROVED



T:1.6 Dimensions of PC board holes
Tolerance : ± 0.05 (Printed-top view)

SCHEMATIC

PJD-035-44D

The schematic shows three terminals: terminal 1 is connected to a ground symbol, terminal 3 is connected to a ground symbol with a checkmark, and terminal 2 is connected to a ground symbol with a tilde symbol.

LEAD FREE

5	SPRING PIECE	1	C5191	Ag PLATED
4	CURVE PIECE	1	C2680	Ag PLATED
3	FLAT PIECE	1	C2680	Ag PLATED
2	AXIS SUPPORT	1	H59(COPPER ALLOY)	Ni PLATED
1	HOUSING	1	PA66	UL-94HB(BLACK)

LTR	PART NAME	QTY	MATERIAL	REMARK
DWN	LILY	DATE	2013.04.11	3RD ANGLE PROJECTION NAME MINIATURE JACK CAT. NO. PJD-035-44D DWN.NO. F-035-44
CHKD	ANDY	DATE	2013.04.11	
APVD	JON	DATE	2013.04.11	

UNLESS OTHERWISE SPECIFIED, TOLERANCE ON DECIMAL: ± 0.3