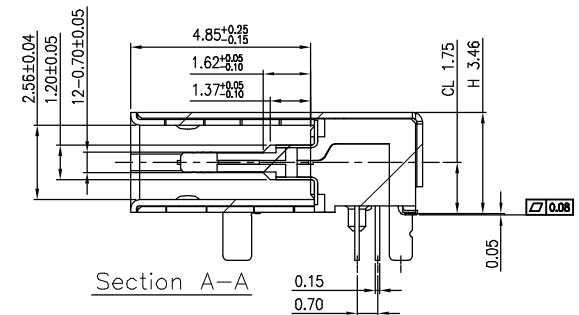
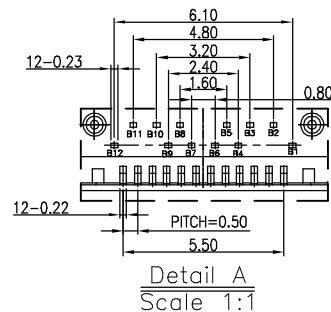
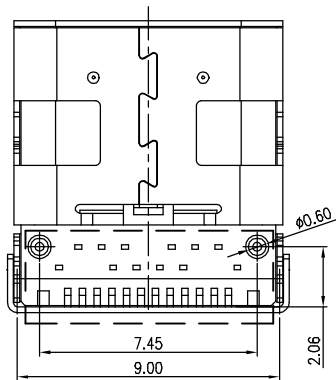
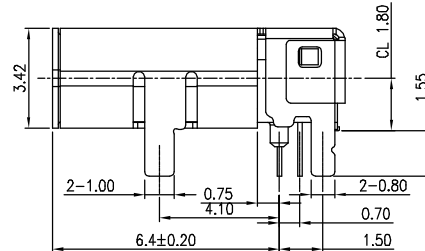
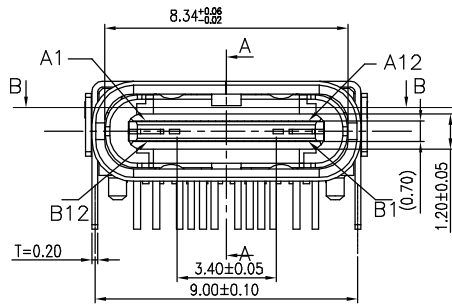
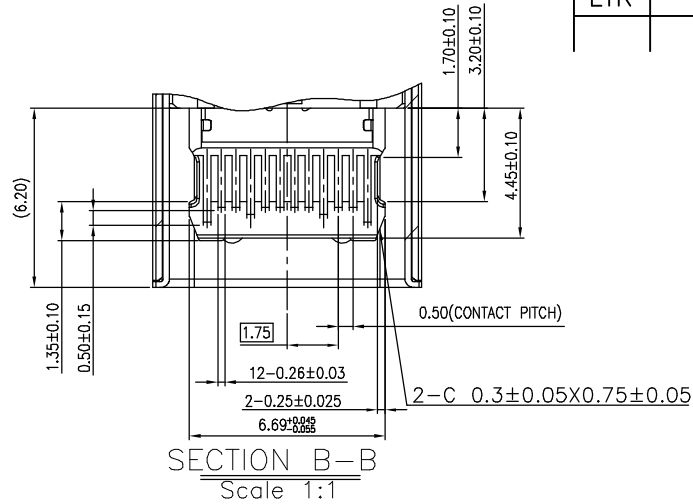
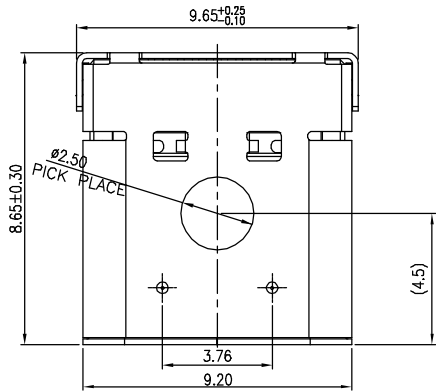


\* RoHS COMPLIANT \*



NOTES:

1. MATERIAL:
  - 1.1 HOUSING: LCP UL94 V-0 BLACK COLOR.
  - 1.2 CONTACT: COPPER ALLOY,C2680.
  - 1.3 SHELL: SUS304-H,T=0.30±0.03mm.
  - 1.4 EMI SHILD:SUS304-H,T=0.12±0.03mm.
  - 1.5 LATCH:SUS301-H,T=0.15±0.03mm.
2. FINISH:
  - 2.1 CONTACT: 1U" AU GOLD PLATING ON CONTACT AREA, G/F AU PLATING ON SOLDER TAILS, 30u" MIN. NICKEL UNDER PLATING OVERALL.
  - 2.2 SHELL: 50u" MIN. NICKEL PLATED.
3. ELECTRICAL SPECIFICATION:
  - 3.1 RATED CURRENT: 1.0A
  - 3.2 RATED VOLTAGE: 30V
  - 3.3 CONTACT RESISTANCE: 50mΩ MAX.
  - 3.4 INSULATION RESISTANCE: 1000MΩ MIN 500V DC
  - 3.5 DIELECTRIC WITHSTANDING VOLTAGE: 500V AC
4. MACHINERY SPECIFICATION:
  - 4.1 MATING FORCE: 10N MIN.  
UNMATING FORCE: 35N MAX.
  - 4.2 DURABILITY: 10000 CYCLES MIN.
5. ENVIRONMENTAL SPECIFICATION:
  - 5.1 OPERATING CONDITION: TEMPERATURE-40°C~+85°C;  
HUMIDITY 80% R.H MAX
  - 5.2 SALT SPRAY: 24H
6. SOLDER ABILITY:255±5°C, 5±0.5s

UNLESS OTHERWISE SPECIFIED, TOLERANCE ON DECIMAL: ±0.3

\* REVISIONS

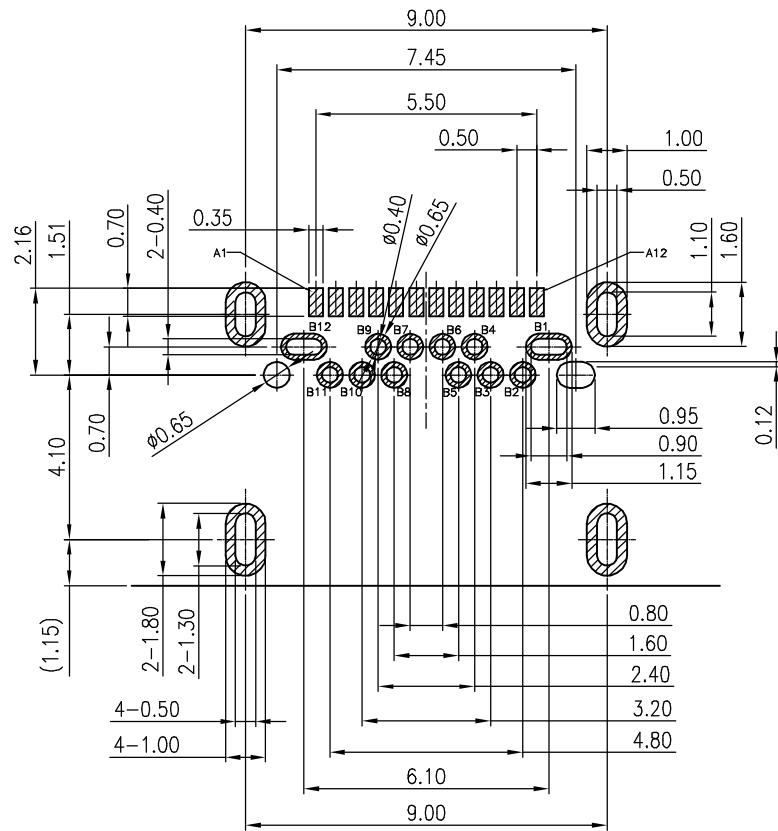
LTR	DESCRIPTION	DATE	REVISED	APPROVED

DWN	LILY	DATE	2024.01.08	3RD ANGLE PROJECTION	NAME	TYPE C 24PIN 雙殼板上DIP+SMT 腳長1.55
CHKD	ANDY	DATE	2024.01.08		CAT. NO.	JCU-104311272
APVD	JON	DATE	2024.01.08	UNIT: mm	DWN.NO.	JBL-104311272
JKCR Electronics Co., Ltd.						001

\* RoHS COMPLIANT \*

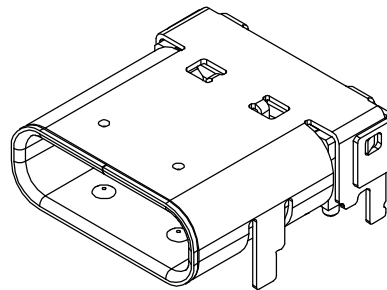
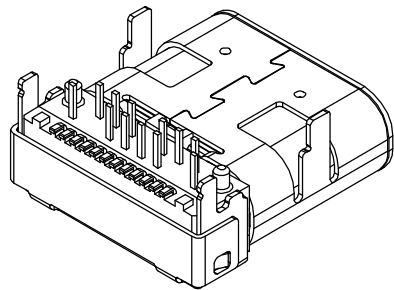
\* REVISIONS

LTR	DESCRIPTION	DATE	REVISED	APPROVED



RECOMMEND P.C.B LAYOUT (COMPONENT SIDE)

TOLERANCE FOR PCB LAYOUT IS ± 0.05



USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use(SBU)	B5	CC2	Configuraaion Channel
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

DWN	LILY	DATE	2024.01.08	3RD ANGLE PROJECTION	NAME	TYPE C 24PIN 雙殼板上DIP+SMT 腳長1.55
CHKD	ANDY	DATE	2024.01.08		CAT. NO.	JCU-104311272
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JKCR Electronics Co., Ltd.					001	